Agilent ADNS-2001 Optical Mouse Sensor

Data Sheet

Description

The ADNS-2001 is a low-cost reflective optical sensor that provides a non-mechanical tracking engine for implementing a computer mouse.

It is based on optical navigation technology which measures changes in position by optically acquiring sequential surface images (frames) and mathematically determining the direction and magnitude of movement. The sensor is mounted in a plastic optical package and designed to be used with the HDNS-2100 (Lens), HDNS-2200 (LED Assembly Clip) and HLMP-ED80 (High Light Output 639 nm LED), providing a complete and compact tracking engine. This optical tracking engine has no moving parts and requires no precision optical alignment enabling high volume system assembly. The ADNS-2001 offers a PS/2 or quadrature output mode for interface flexibility. Resolution is specified as 400 cpi at rates of motion up to 16 inches per second.

Features

- **Optical navigation technology**
  - Superior precision and smooth navigation optimized for desktop and portable mouse applications
  - No mechanical moving parts, provides high reliability and needs no maintenance
- **Complete compact 2-D motion sensor**
  - Easy implementation and design flexibility
  - Replaces mechanical ball system in traditional mice
- **Two selectable output modes**
  - Two channel quadrature output mode (X and Y direction) which emulates encoder phototransistors
  - Standard 3-button PS/2 output mode
- **High speed motion detector**
  - Accurately measures up to 16 inches per second at 400 cpi
- **Accurate navigation over a wide range of surfaces**
  - Enables mouse to be used with or without a mouse pad
- **Power conservation mode during no motion**
- **Compatible with high volume manufacturing processes**
  - Requires no precision optical alignment
  - Wave solderable
- **33% faster than HDNS-2000**
  - 2000 fps (@ 24 MHz)

Applications

- **Computer mice for desktop PCs, workstations, and portable computers**
- **Trackball**
- **Integrated input devices**

***Caution**: It is advised that normal static precautions be taken in handling and assembly of this component to prevent damage and/or degradation which may be induced by ESD.*

Theory of Operation

The ADNS-2001 is based on Optical Navigation Technology. It contains an Image Acquisition System (IAS), Digital Signal Processor (DSP), and a mode selectable PS/2 or quadrature output converter. The IAS acquires images of microscopic surface images via the lens and illumination system provided by the HDNS-2100, HDNS-2200, and the HLMP-ED80. These images are further processed by the DSP to determine direction and distance of motion. The DSP generates a stream of delta x and delta y relative displacement values which are then communicated to the output converter. This converter provides a PS/2 3-button output, replacing existing mouse microcontrollers, or two channel quadrature output, for direct interface to existing mouse microcontrollers.

While the part can be run at 24 MHz in quadrature mode, 18 MHz is recommended for PS/2 mode.

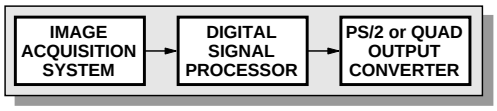


Figure 1. ADNS-2001 block diagram.

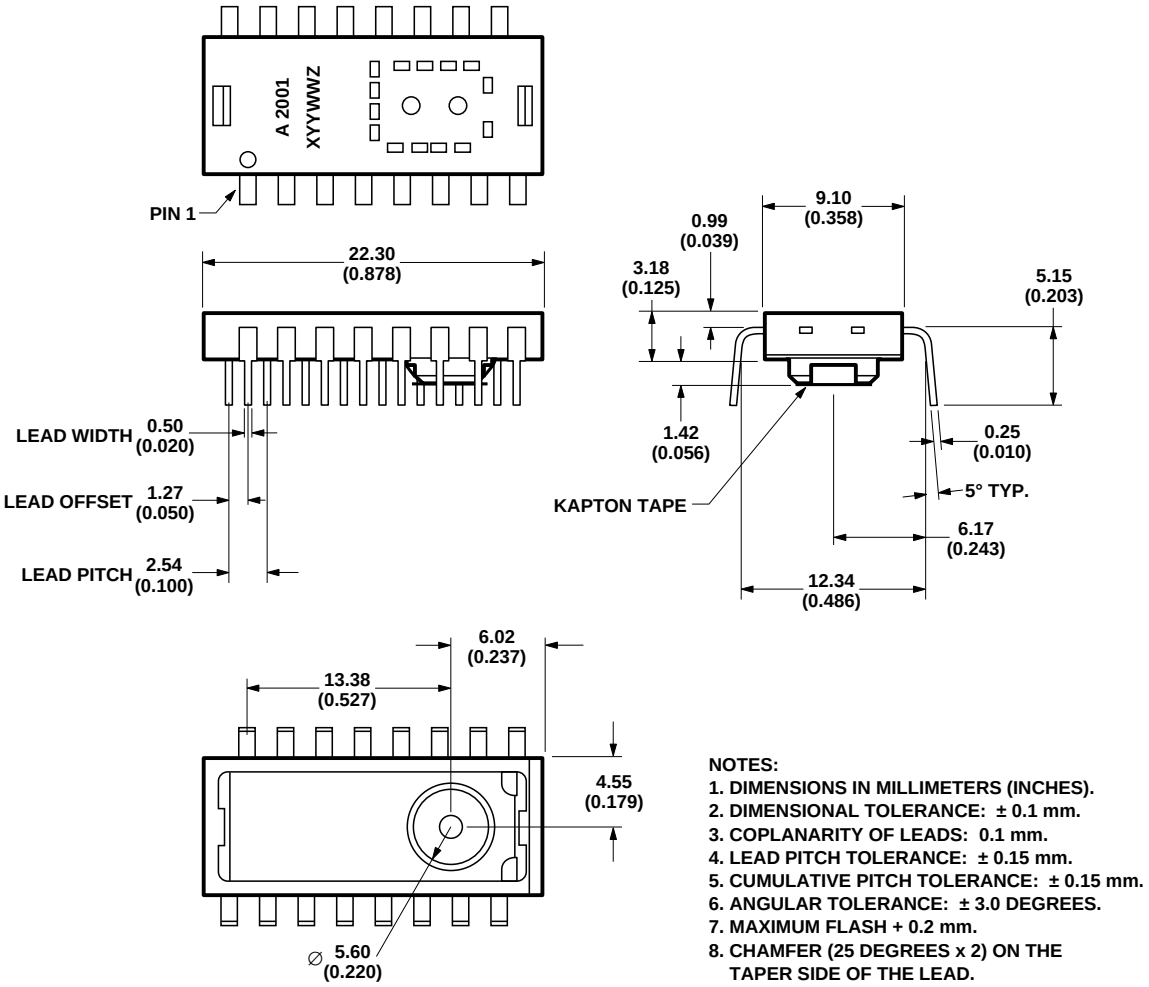


Figure 2. ADNS-2001 sensor package outline drawing.

Pinout

Pin	Name	PS/2 Mode	Quadrature Mode
1	PS2_C	PS/2 Interface Clock	PS/2 Interface Clock
2	MODE/XA	Select PS/2 Mode	XA Output
3	RB/XB	Right Button Input	XB Output
4	MB/YB	Middle Button Input	YB Output
5	LB/YA	Left Button Input	YA Output
6	XY_LED	LED Control Output	LED Control Output
7	VDD3	3.3 VDC Input	3.3 VDC Input
8	REFB	Internal Reference	Internal Reference
9	OSC1	Oscillator Input	Oscillator Input
10	GND	Ground	Ground
11	OSC2	Oscillator Output	Oscillator Output
12	GND	Ground	Ground
13	VDD5	5 VDC Input	5 VDC Input
14	VDD5	5 VDC Input	5 VDC Input
15	NRESET	NRESET	NRESET
16	PS2_D	PS/2 Interface Data	PS/2 Interface Data

2D Assembly Drawing of ADNS-2001

Shown with HDNS-2100, HDNS-2200, and HLMP-ED80.

Agilent provides an IGES file drawing describing the base plate molding features for lens and PCB alignment. Please contact sales representative or visit our web site. Also, see HDNS-2100 Technical Data Sheet for more information.

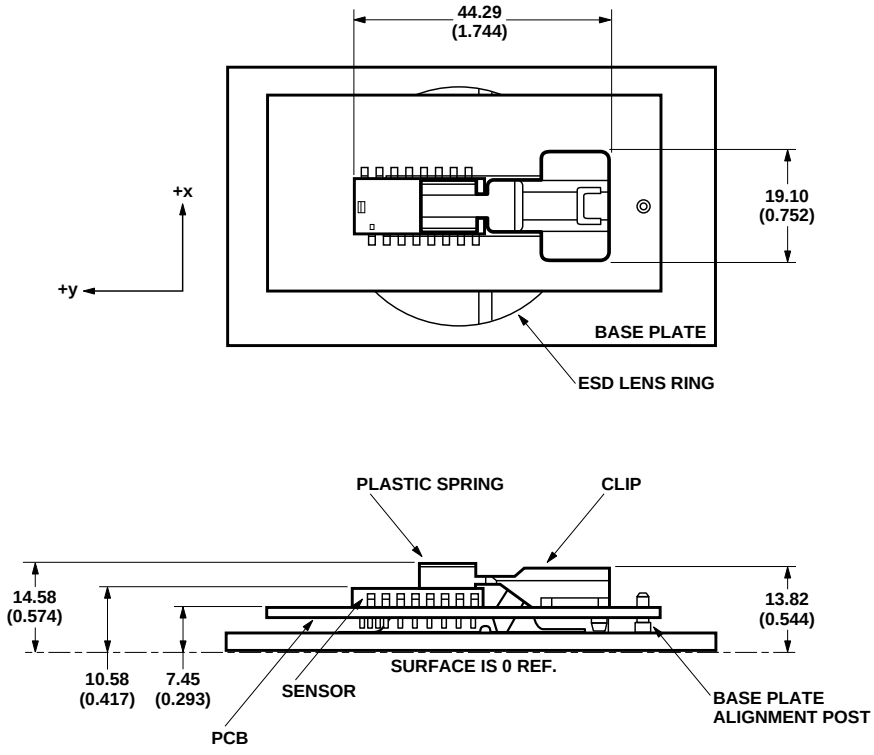


Figure 3.

Exploded View Drawing

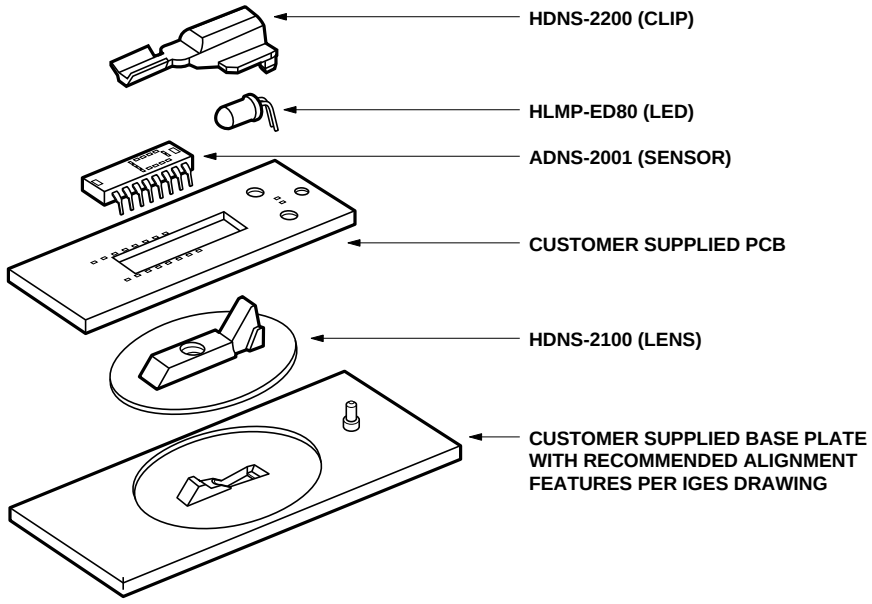
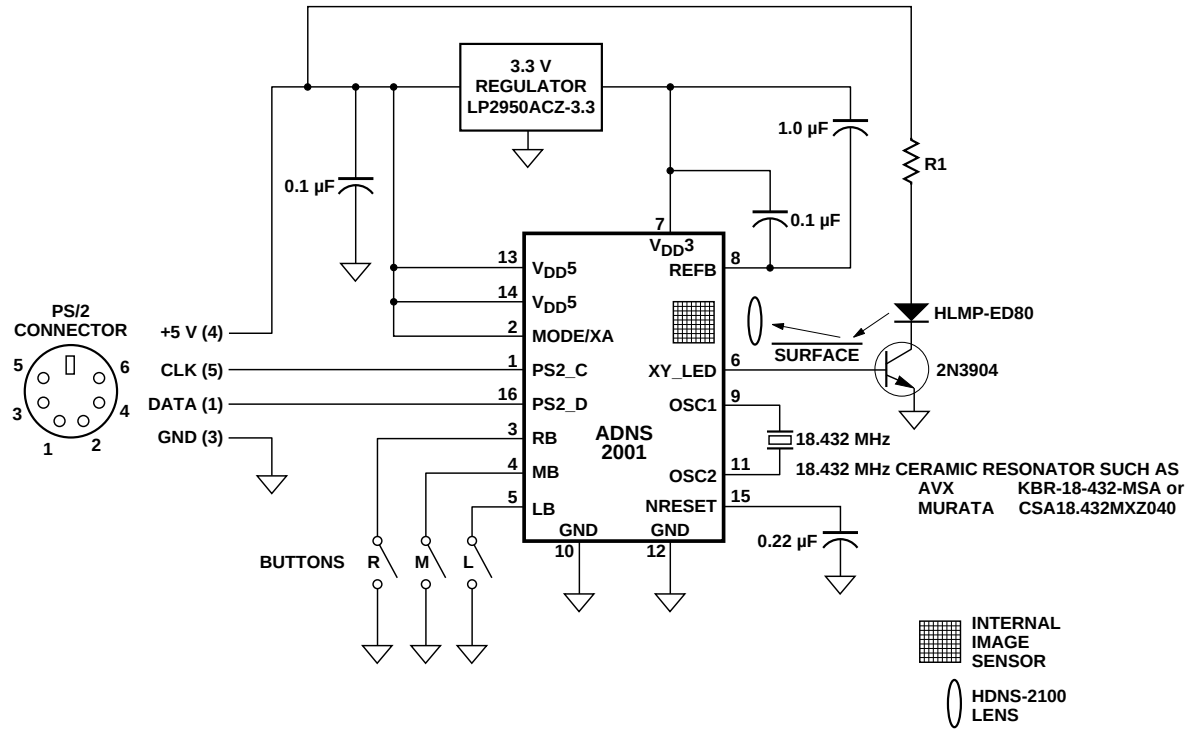


Figure 4.

Typical Application using PS/2 Output

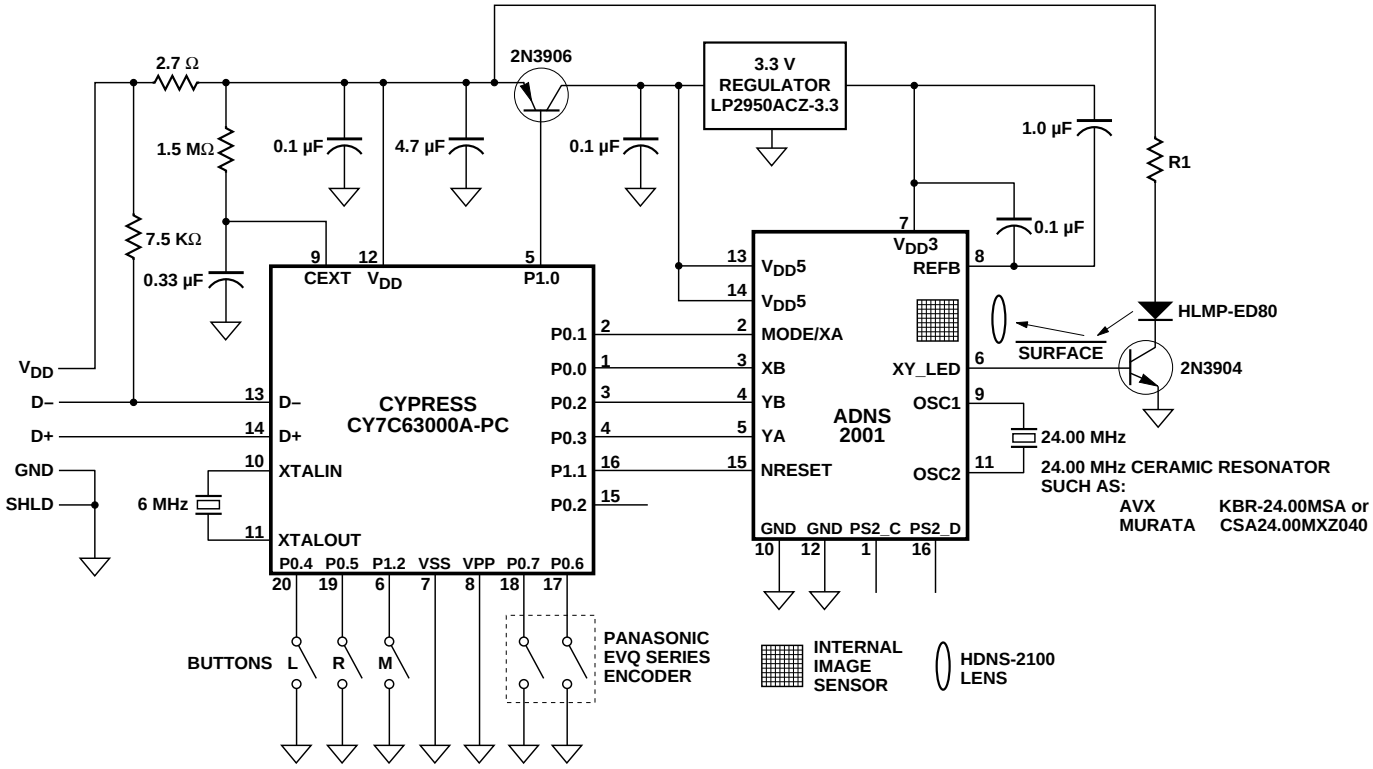
18 MHz Operation for generic PS/2 compatibility



Note:
0.1 µF between pins 7 and 8 must be ceraminc, and must be trace lengths less than 5 mm.

Typical USB Application using Quadrature Output

18 or 24 MHz Operation



Notes:

1. Due to the Cypress implementation of USB suspend mode support, the NRESET pin of the ADNS-2001 must be reset using a line from the Cypress chip. The reason for this is that the Cypress chip does not configure the port input pins until after it has received a bus reset from the USB port. The unconfigured input port pins present a 16 KΩ pullup to V_{CC}. If a cap is used on NRESET (pin 15), this pullup will result in the ADNS-2001 seeing a high on the MODE pin and powering up in the PS/2 mode.
2. The quadrature input pins of the Cypress part must be programmed to be Hi-Z, instead of the normal current pulldowns. This ensures that the ADNS-2001 will be able to pull the quadrature lines high over all conditions of voltage and temperature.
3. 0.1 µF between pins 7 and 8 must be ceramic, and must be trace lengths less than 5 mm.

Recommended LED Bin Table

LED Bin Category	R1 Value
K	69.8 Ω
L	69.8 Ω
M	69.8 Ω
N	69.8 Ω
P	69.8 – 78.7 Ω
Q	69.8 – 93.1 Ω
R	69.8 – 113 Ω
S	69.8 – 137 Ω
T	69.8 – 169 Ω

The 69.8 Ω resistor is determined by the absolute maximum rating of 50 mA for the HLMP-ED80. The other resistor values for brighter bins will guarantee good signals with reduced power.

For the IEC 60825-1 eye safety consideration, please contact sales representative for the technical report.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Notes
Storage Temperature	T_S	–40	85	°C	
Operating Temperature	T_A	0	40	°C	
Lead Solder Temperature			260	°C	For 10 seconds, 1.6 mm below seating plane (see HLMP-ED80 data sheet for LED solder specifications)
Supply Voltage	V_{DD3}	–0.5	3.6	V	
Supply Voltage	V_{DD5}	–0.5	5.5	V	
ESD			2	kV	All pins, Human Body Model
Input Voltage	Vin	–0.5	V_{DD5} + 0.5	V	All I/O except OSC1 and OSC2
Input Voltage	Vin	–0.5	V_{DD3} + 0.5	V	OSC1 and OSC2

Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
Operating Temperature	T_A	0		40	°C	
Supply Voltage	V_{DD3}	3.15	3.3	3.45	V	
Supply Voltage	V_{DD5}	4.25	5.0	5.5	V	
Clock Frequency	CLK	23.88	24.00	24.12	MHz	Set by ceramic resonator
		17.4	18.432	18.7		For generic PS/2 operation
Resonator Impedance	X_{RES}			40	Ω	
Reset Capacitor	C_{RESET}	0.001	0.22	10.0	µF	
Distance from Lens Reference Plane to Surface	A	2.3	2.4	2.5	mm	Dimension A on HDNS-2100 data sheet
Speed	S	0		16	in/sec	
		0		39	cm/sec	
Acceleration	ACC	0		0.2	g	
Light Level onto IC	IRR_{INC}			2500	mW/m^2	λ = 639 nm
		40		0		

DC Electrical Specifications

Electrical Characteristics over recommended operating conditions. Typical values at 25°C, V_{DD3} = 3.3, V_{DD5} = 5.0, Clock = 24 MHz.

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
Supply Current (Mouse Moving)	I_{DD3}		12.1	20.1	mA	
Supply Current (Mouse Moving)	I_{DD5}		6.1	12.4	mA	Pin 6 = 0.6 V
Supply Current (Mouse Not Moving)	I_{DD5}		2.5		mA	Pin 6 = 0.6 V
Input Low Voltage	V_{IL}			0.8	V	
Input High Voltage	V_{IH}	2			V	
Output Low Voltage (LED)	V_{OL}		0.3	0.5	V	I_{OL} = 2 mA
Output Low Voltage (XA, XB, YA, YB)	V_{OL}			0.5	V	I_{OL} = 4 mA
Output High Current (XA, XB, YA, YB)	I_{OH}	100	300	600	µA	V_{OH} = 2.1 V
Output High Current (LED)	I_{OHBD}	1.5	3.1	6	mA	V_{BE} = 0.6 V
Input Pullup (RB, MB, LB)	I_{PU}	100	300	600	µA	V_{IN} = 0.8 V
Output Low Voltage (PS/2)	V_{OL}		0.41	0.5	V	I_{OL} = 10 mA
Output Pullup Current (PS/2)	I_{OH}	300	625	1500	µA	V_{OH} = 2.0 V
Reset Pullup Current	I_{RESET}	5	10	20	µA	V_{NRESET} = 2.0 V

I/O Specifications

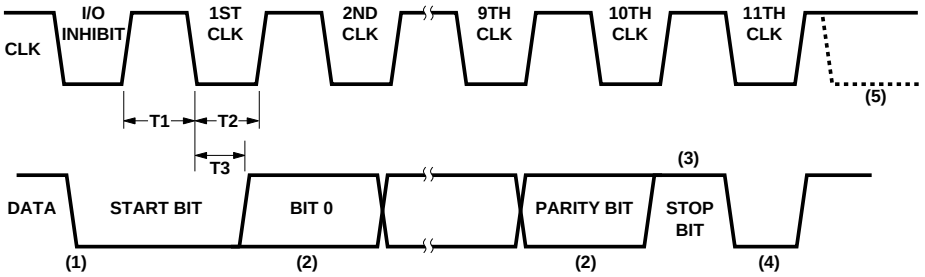
PS/2 Command Set Implementation

The following commands are implemented. All other commands will cause an FE (resend) response from the ADNS-2001. A second invalid command will cause an FC (error) response from the ADNS-2001.

Mnemonic for Command	Hex Command and Response Bytes	Mnemonics for Command and Response Bytes	Description	Valid Values and Default Value after Software or Hardware Reset
RESET	FF **FA AA 00**	FF ACK ID DT	Soft reset ID = AA, DT = 0	
RESEND	FE **nn...**	FE nn...	Resend last byte (i.e., ACK) or packet	
SET_DFS	F6 **FA**	F6 ACK	Default setting	
DISABLE	F5 **FA**	F5 ACK	Disable stream mode	**(default mode)**
ENABLE	F4 **FA**	F4 ACK	Enable stream mode	
SET_SAMPLING	F3 **FA** nn **FA**	F3 ACK nn ACK	Set sampling rate	(0A 14 28 3C 50 **64** C8) 10 20 40 60 80 **100** 120 reports/second
READ_DT	F2 **FA 00**	F2 ACK DT	Responds with DT = 00	
ECHO	EE **FA**	EE ACK	Echo all further commands until NO_ECHO or RESET	
NO_ECHO	EC **FA**	EC ACK	Respond to following commands normally	
READ_DATA	EB **FA nn nn nn**	EB ACK nn nn nn	Request a data packet	See IBM PS/2 Mouse Technical Reference
SET_STREAM	EA **FA**	EA ACK	Respond with data packets at the sample rate	**(default mode)**
SET_PROMPT	F0 **FA**	F0 ACK	Data only sent on READ_DATA	
STATUS	E9 **FA nn nn nn**	E9 ACK nn nn nn	Request status packet	See IBM PS/2 Mouse Technical Reference
SET_SCALE	E7 **FA**	E7 ACK	Pseudo log	
LIN_SCALE	E6 **FA**	E6 ACK	Linear	**(default mode)**
SET_RES	E8 **FA** nn **FA**	E8 ACK nn ACK	Set resolution	(00 01 **02** 03) 2 4 **8** 16 counts/mm)
DISABLE_TEST	E8 **FA** AA **FA**	E8 ACK AA RESEND	For test purposes only	Default mode after hardware reset

PS/2 Mode Output Waveforms @ 24 MHz

Host Sending Data Timing Diagram

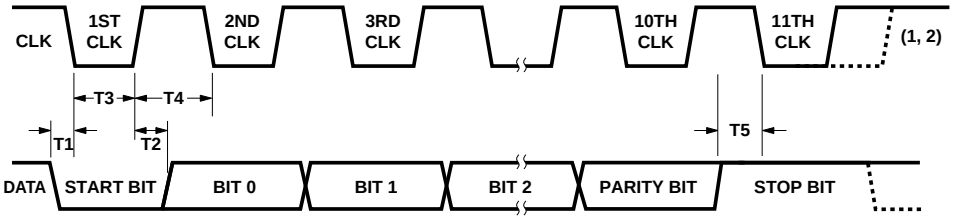


Notes:

1. The mouse checks the DATA line. If the line is low, the system has data to transmit. The DATA line is set inactive when the start bit (always 0) is placed on the DATA line.
2. The mouse samples the DATA line for each bit while the CLK line is high. Data must be stable within 1 microsecond after the rising edge of the CLK line.
3. The mouse checks for a high stop bit after the 10th CLK. If the DATA line is low, the mouse continues to clock until the DATA line becomes high, then clocks the line-control bit, and at the next opportunity sends a Resend command to the system.
4. The mouse pulls the DATA line low, producing the line-control bit.
5. The host can pull the CLK line low, inhibiting the mouse.

Timing Parameter	Description	Min. Time	Max. Time
T1	Duration of CLK high	22.5 µsec	37.5 µsec
T2	Duration of CLK low	22.5 µsec	37.5 µsec
T3	Time from falling CLK transition, to date transition	0 µsec	22.5 µsec

Host Receiving Timing Diagram



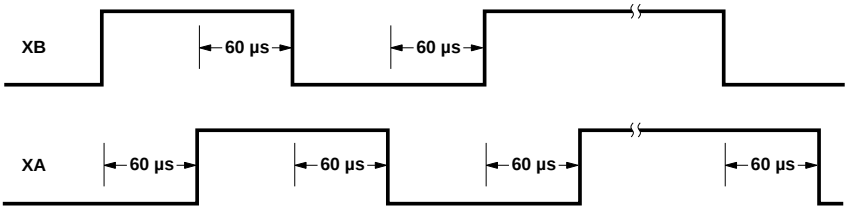
Notes:

1. The host can hold the clock signal low to inhibit the next transmission.
2. The host raises the clock line to allow the next transmission.
3. All times given below assume a 24 MHz resonator and are dependent upon its accuracy.

Timing Parameter	Description	Min. Time	Max. Time
T1	Time from DATA transition to falling edge of CLK	3.75 µsec	18.75 µsec
T2	Time from rising edge of CLK to DATA transition	3.75 µsec	18.75 µsec
T3	Duration of CLK low	22.5 µsec	37.5 µsec
T4	Duration of CLK high	22.5 µsec	37.5 µsec
T5	Time to mouse inhibit after clock 11 to ensure the mouse does not start another transmission	0 µsec	37.5 µsec

Quadrature Output Mode Waveform @ 24 MHz

The 2 channel quadrature outputs are 5 V CMOS outputs. The Delta X count is used to generate the XA and XB quadrature signals. The Delta Y count is used to generate the YA and YB quadrature signals. Delta X, Y counts are in the range of +7 to –7 counts of motion and new Delta X, Y values are generated at a rate of 2000 Hz. The quadrature signals can change at a maximum rate of 16.7 kHz.



Example: Quadrature Output Waveform (+X motion).

AC Electrical Specifications

Electrical Characteristics over recommended operating conditions. Typical values at 25°C, V_{DD3} = 3.3, V_{DD5} = 5.0, A = 2.4 mm, CLK = 24.00 MHz.

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
PS/2 Baud Rate	Fps2	13.3	16.7	20	Kbaud	
PS/2 Data Transition Delay after PS/2_C Rising Edge	T2	7.5	15	18.8	µs	See PS/2 timing diagrams
PS/2 Motion Report Rates			133		reports/sec	See PS/2 command settings
Motion Scale Factor			400		counts/inch	
Power Up Delay				100	ms	C_{RESET} = 0.22 µF

Typical Performance Characteristics

Typical Performance of ADNS-2001 assembled as shown in Figure 3 with HDNS-2100 Lens, HDNS-2200 LED Assembly Clip and HLMP-ED80.

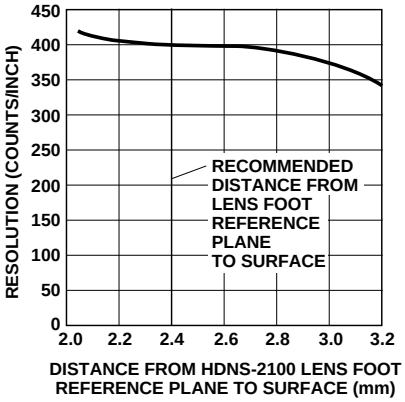


Figure 5. Typical resolution vs. assembly height.

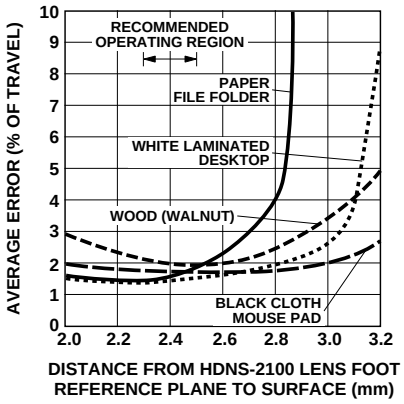


Figure 6. Typical error vs. assembly.

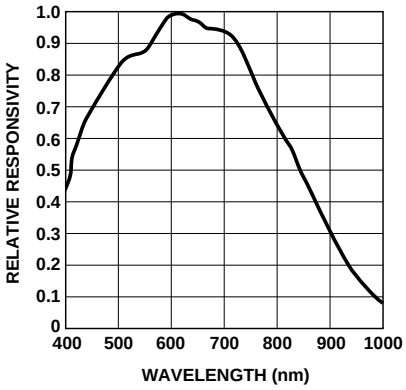


Figure 7. Typical responsivity vs. wavelength.

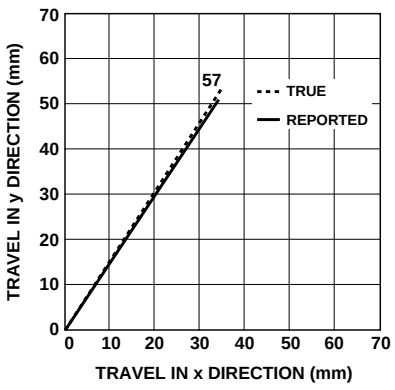


Figure 8. Typical reported path vs. true path.

Note:
Due to the higher flame speed, any shorter wavelength LED other than HLMP-ED80 is not recommended.

Ordering Information

Specify Part Number as follows:

ADNS-2001 = Sensor IC in a 16-pin optical plastic package, 20 per tube, 1000 pieces in a box.

ADNB-2012 = ADNS-2001 Sensor and HDNS-2100 Round Lens Bundle Kit, 1000 pieces incremental (e.g., ADNB-2012: 1000 pieces = 1000 pieces of ADNS-2001 and 1000 pieces of HDNS-2100 in a box).

ADNB-2013 = ADNS-2001 Sensor and HDNS-2100 #001 Trimmed Lens Bundle Kit, 1000 pieces incremental (e.g., ADNB-2013: 1000 pieces = 1000 pieces of ADNS-2001 and 1000 pieces of HDNS-2100 #001 in a box).

HDNS-2100 = Round Optical Mouse Lens

HDNS-2101-001 = Trimmed Optical Mouse Lens

HDNS-2200 = LED Assembly Clip (Black)

HDNS-2200-001 = LED Clip (Clear)

HLMP-ED80 = LED

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